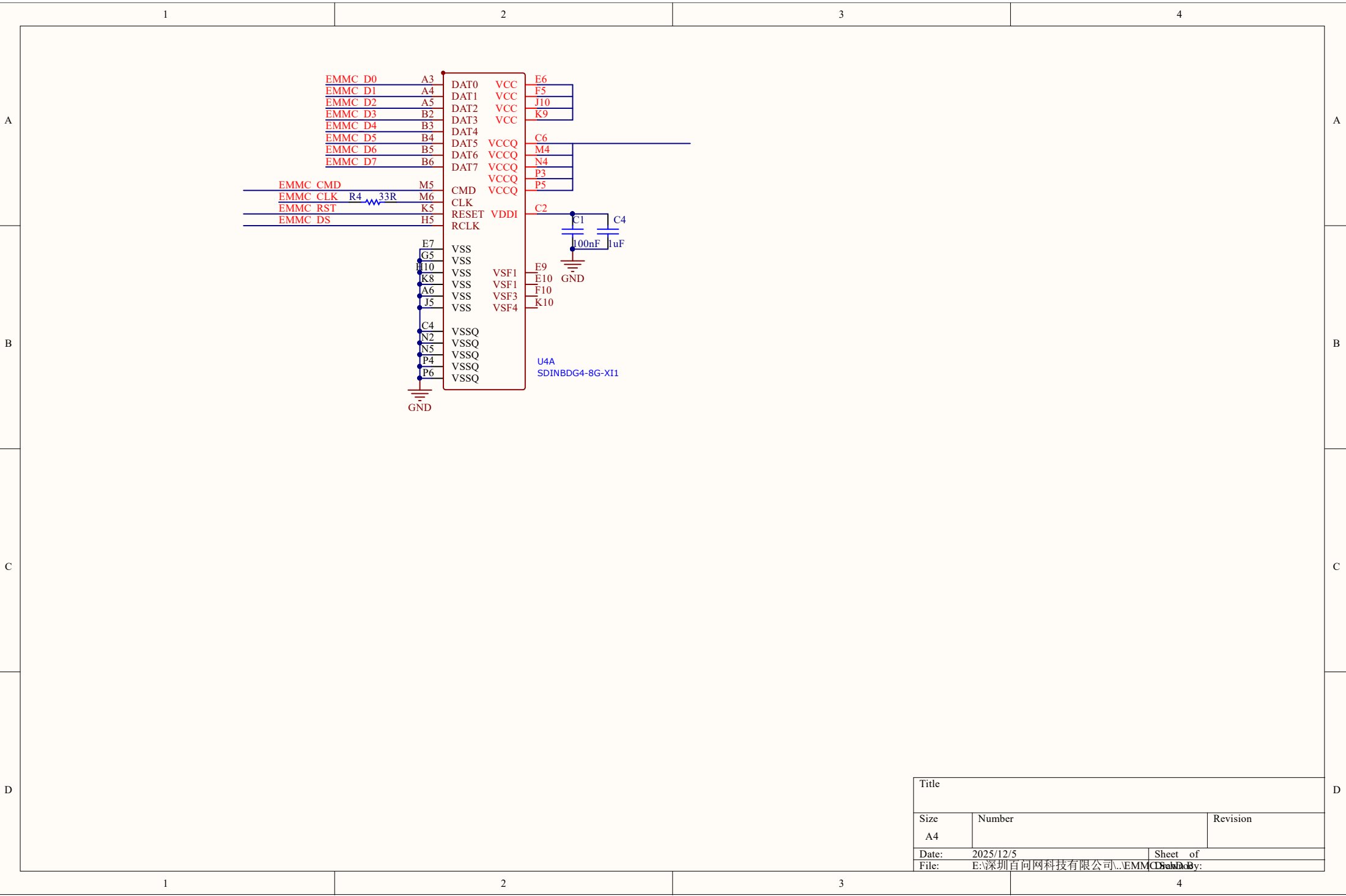
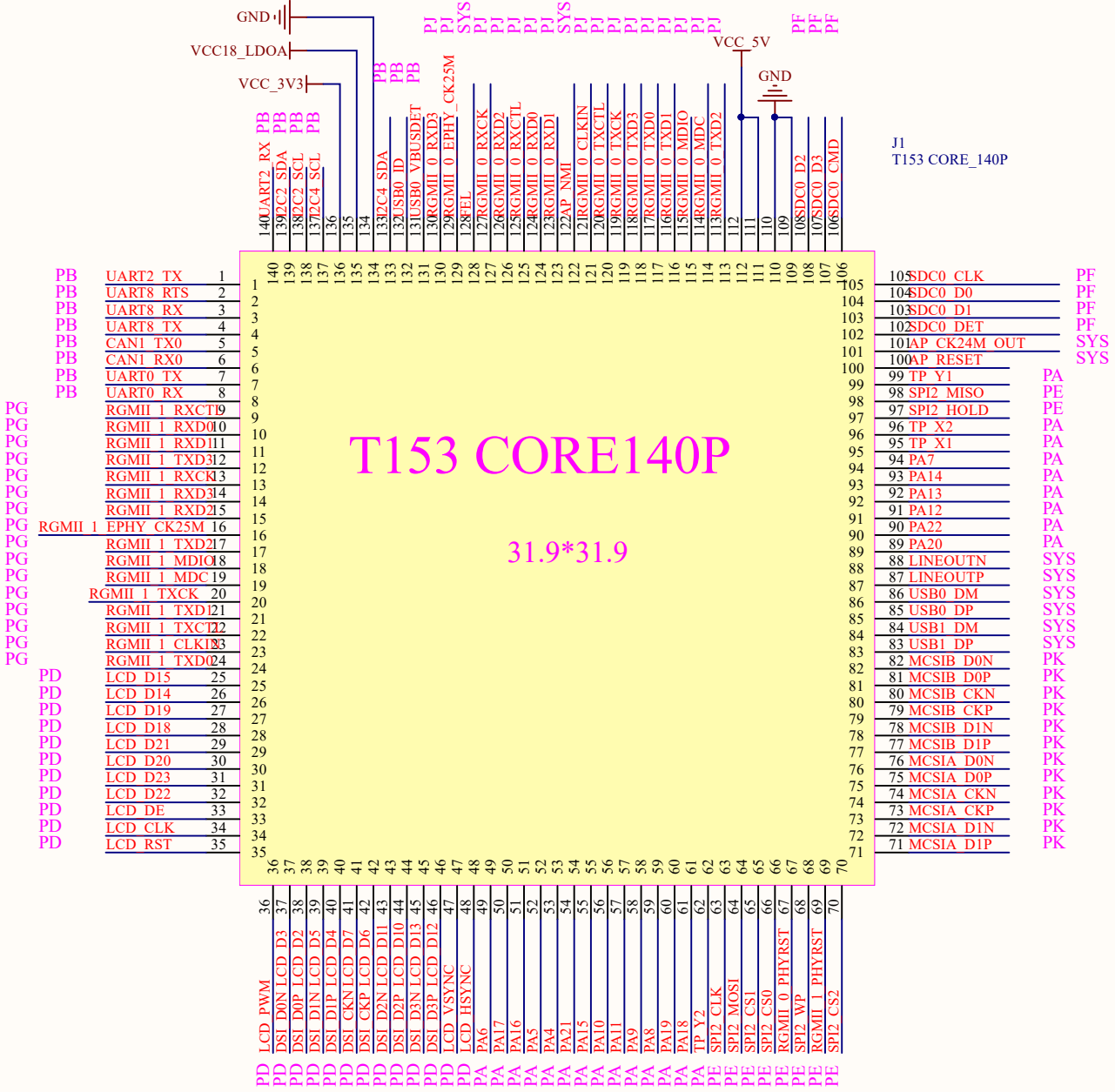
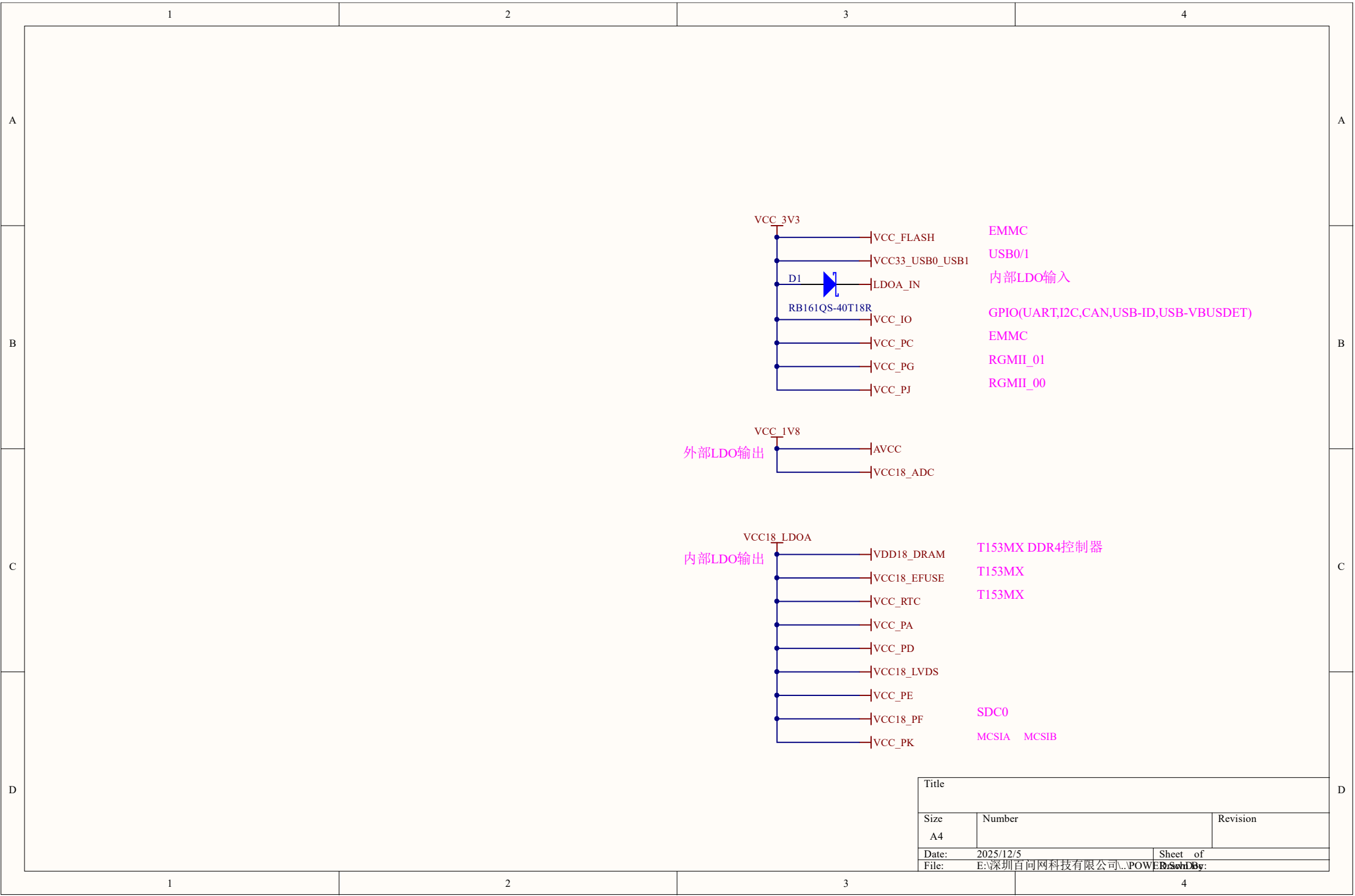




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U1F

GPIO-A

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PA0/GPADC2-0/TP-X1/LBUS-READY3/LBUS-LBE2/PA-EINT0
PA1/GPADC2-1/TP-X2/LBUS-READY2/LBUS-DP1/PA-EINT1
PA2/GPADC2-2/TP-Y1/LBUS-DP2/PA-EINT2
PA3/GPADC2-3/TP-Y2/LBUS-DP3/PA-EINT3
MI0_00-RXD1/RMII0-RXD1/NCSI-HSYNC/LBUS-CLK/PA-EINT4
MI1_00-RXD0/RMII0-RXD0/NCSI-VSYNC/LBUS-ALE/PA-EINT5
CRS-DV/NCSI-MCLK/LBUS-CSI1/PWMCS-19/LCD-D0/PA-EINT6
I0-RXER/NCSI-PCLK/LBUS-CS0/PWMCS-19/LCD-D1/PA-EINT7
MI10-TXD1/NCSI-D0/LBUS-DP0/PWMCS-1A/LCD-D8/PA-EINT8
NCSSI-D1/LBUS-DP1/PWMCS-1B/LBUS-LBE3/LCD-D9/PA-EINT9
I0-TXCK/NCSSI-D2/LBUS-LBE0/PWMCS-1C/LCD-D16/PA-EINT10
I0-TXEN/NCSSI-D3/LBUS-LBE1/PWMCS-1D/LCD-D17/PA-EINT11
I-8/RGMII_00-MDC/NCSSI-D4/LBUS-LD8/PWMCS-1E/PA-EINT12
I9/RGMII_00-MDIO/NCSSI-D5/LBUS-LD9/PWMCS-1F/PA-EINT13
I00-EPHY-25/50M/NCSSI-D6/LBUS-LD10/UART6-TX/PA-EINT14
XD3/RMII0-NUL1/NCSSI-D7/LBUS-LD11/UART6-RX/PA-EINT15
XD2/RMII0-NUL1/NCSSI-D8/LBUS-LD12/TWI3-SCK/PA-EINT16
XCK/RMII0-NUL1/NCSSI-D9/LBUS-LD13/TWI3-SDA/PA-EINT17
GMII_00-TXD3/RMII0-NUL1/NCSSI-D10/LBUS-LD14/PA-EINT18
GMII_00-TXD2/RMII0-NUL1/NCSSI-D11/LBUS-LD15/PA-EINT19
PA20/GPADC1-6/NCSSI-D12/LBUS-WAIT/PA-EINT20
PA21/GPADC1-7/NCSSI-D13/LBUS-CS3/SPI2-CS1/PA-EINT21
PA22/GPADC1-8/NCSSI-D14/LBUS-CS2/PA-EINT22
PA23/GPADC1-9/NCSSI-D15/LBUS-DE/PA-EINT23

```

IC13	TP	X1
IC14	TP	X2
IC12	TP	Y1
E20	TP	Y2
J21	PA	4
J20	PA	5
K19	PA	6
1D14	PA	7
G19	PA	8
G21	PA	9
H19	PA	10
G20	PA	11
1F13	PA	12
1D13	PA	13
1D12	PA	14
H20	PA	15
J19	PA	16
K20	PA	17
F19	PA	18
F20	PA	19
1G14	PA	20
1F14	PA	21
1G13	PA	22
1F12	DDR	PARA SEL

[illegible]

VCC-PA
VCC18-ADC
AGND-ADC

T153MX
U1G

GPIO-B

GPIO-B

PB0/UART2-TX/SPI3-CS0/JTAG-MS/PWM0-6/RJTAG-MS/RGMII_02-RXD1/RMII2-RXD1/PB-EINT0
PB1/UART2-RX/SPI3-CLK/JTAG-CK/PWM0-7/RJTAG-CK/RGMII_02-RXD0/RMII2-RXD0/PB-EINT1
PB2/UART2-RTS/SPI3-MOSI/JTAG-D0/TWI2-SCK/JTAG-DO/RGMII_02-RXCTL/RMII2-CRS-DV/PB-EINT2
PB3/UART2-CTS/SPI3-MISO/JTAG-D1/TWI2-SDA/RJTAG-DI/RGMII_02-CLKIN/RMII2-RXER/PB-EINT3
PB4/TWI1-SCK/I2S0-MCLK/SPI3-CS1/PWM1-0/IR0-RX/UART8-TX/RGMII_02-TXD1/RMII2-TXD1/PB-EINT4
PB5/TWI1-SDA/I2S0-BCLK/SPI3-CS2/PWM1-1/IR1-RX/UART8-RX/RGMII_02-TXD0/RMII2-TXD0/PB-EINT5
PB6/I2S0-LRCK/PWM1-2/IR1-UART8-RTS/TWI0-SCK/RGMII_02-TXCK/RMII2-TXCK/PB-EINT6
PB7/OWA-IN/I2S0-DOUT0/I2S0-DIN1/PWM1-3/CAN1-TX/UART8-CTS/TWI0-SDA/RGMII_02-TXCL/RMII2-TXEN/PB-EINT7
PB8/OWA-OUT/I2S0-DIN0/I2S0-DOUT1/PWM0-0/CAN1-RX/UART8-DCD/PB-EINT8
PB9/UART0-TX/I2S0-DIN2/I2S0-DOUT2/TWI0-SCK/UART8-DSCR/PB-EINT9
PB10/UART0-RX/I2S0-DIN3/I2S0-DOUT3/PWM0-1/TWI0-SDA/UART8-DTR/PB-EINT10
PB11/TWI5-SCK/UART7-RTS/SPI3-CS0/PWM0-2/WATCHDOG-SIG/UART8-R/RGMII_02-MDIO/PB-EINT11
PB12/TWI5-SDA/UART7-CTS/SPI3-CLK/PWM0-3/PLL-LOCK-DBG/RGMII_02-MDIO/PB-EINT12
PB13/TWI4-SCK/UART7-TX/SPI3-MOSI/PWM0-4/IR2-RX/CLK-FANOUT0/RGMII_02-EPHY2-50M/PB-EINT13
PB14/TWI4-SDA/UART7-RX/SPI3-MISO/PWM0-5/IR3-RX/CLK-FANOUT1/PB-EINT14

H2	UART2 TX
G3	UART2 RX
G2	I2C2 SCL
G1	I2C2 SDA
J2	UART8 TX
J1	UART8 RX
H3	UART8 RTS
J3	CAN1 TX0
K2	CAN1 RX0
K3	UART0 TX
L1	UART0 RX
F3	USB0 ID
F2	USB0 VBUSDET
1B1	I2C4 SCL
1B2	I2C4 SDA

[illegible]

VCC-IO — 1B4 — VCC_IO

3.3V

T153MX

U1H

GPIO-C

GPIO-C PC0/SDC2-CLK/SPI0-CLK/SPIF0-CLK/PC-EINT0
PC1/SDC2-CMD/SPI0-CS0/SPIF0-CS0/PC-EINT1
PC2/SDC2-D2/SPI0-MOSI/SPIF0-MOSI/BOOT-SEL0/PC-EINT2
PC3/SDC2-D0/SPI0-MISO/SPIF0-MISO/BOOT-SEL1/PC-EINT3
PC4/SDC2-D0/SPI0-WP/SPIF0-WP/CAN1-TX0/UART6-TX/PC-EINT4
PC5/SDC2-D3/SPI0-HOLD/SPIF0-HOLD/CAN1-RX0/UART6-RX/PC-EINT5
PC6/SDC2-D4/SPI0-CS1/SPIF0-D4/CAN0-TX0/PC-EINT6
PC7/SDC2-D5/SPIF0-D5/CAN0-RX0/PC-EINT7
PC8/SDC2-D6/SPIF0-D6/UART5-CTS/PC-EINT8
PC9/SDC2-D7/SPIF0-D7/UART5-TX/PC-EINT9
PC10/SDC2-D5/SPIF0-DQS/UART5-RX/PC-EINT10
PC11/SDC2-RST/UART5-RTS/PC-EINT11

T2	EMMC CLK	
R3	EMMC CMD	
Y1	EMMC D2	BOOT_SEL0
W1	EMMC D1	BOOT_SEL1
V2	EMMC D0	
U2	EMMC D3	
U3	EMMC D4	
V3	EMMC D5	
W2	EMMC D6	
Y2	EMMC D7	
U1	EMMC DS	
T3	EMMC RST	

VCC-PC — 1J1 3.3V — VCC PC

BOOT_SEL1	BOOT_SEL0	SPI0-NAND->SPI0 NOR(4线)->SPI0 NOR(1线)->USB
0	0	SPI0 NOR(4线)->SPI0 NOR(1线)->SPI0-NAND->USB
0	1	SDC0->SPI0-NAND->SPI0 NOR(4线)->SPI0 NOR(1线)->USB
1	0	SDC0->SPI2 NOR(4线)->SPI2 NOR(1线)->SPI2-NAND->EMMC2_USR->EMMC2_BOOT->SPI0_NAND->USB
1	1	说明: BOOT_SEL在芯片内部默认上拉, PIN浮空时为高电平

T153MX

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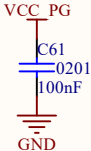
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GPIO-G

PG0/SDC1-CLK/UART8-TX/RGMII_01-RXD3/RMII1-NULL/PG-EINT0
PG1/SDC1-CMD/UART8-RX/RGMII_01-RXD2/RMII1-NULL/PG-EINT1
PG2/SDC1-D0/UART8-RTS/RGMII_01-RXCK/RMII1-NULL/PG-EINT2
PG3/SDC1-D1/UART8-CTS/RGMII_01-TXD3/RMII1-NULL/PG-EINT3
PG4/SDC1-D2/CAN0-TX0/RGMII_01-TXD2/RMII1-NULL/PG-EINT4
PG5/SDC1-D3/CAN0-RX0/RGMII_01-EPHY-25/50M/PG-EINT5
PG6/UART1-TX/TW14-SCK/UART9-CTS/RGMII_01-RXD1/RMII1-RXD1/I2S2-MCLK/PG-EINT6
PG7/UART1-RX/TW14-SDA/UART9-RTS/RGMII_01-RXD0/RMII1-RXD0/I2S2-BCLK/PG-EINT7
PG8/UART1-RTS/TW15-SCK/UART9-TX/RGMII_01-RXCTL/RMII1-CRS-DV/I2S2-LRCK/PG-EINT8
PG9/UART1-CTS/TW15-SDA/UART9-RX/RGMII_01-CLKIN/RMII1-RXER/I2S2-DOU0/I2S2-DIN1/PG-EINT9
PG10/UART4-TX/I2S1-MCLK/TW12-SCK/RGMII_01-TXD1/RMII1-TXD1/I2S2-DIN0/I2S2-DOU1/PG-EINT10
PG11/UART4-RX/I2S1-BCLK/TW12-SDA/RGMII_01-TXD0/RMII1-TXD0/DMIC-CLK/PG-EINT11
PG12/UART4-RTS/I2S1-LRCK/RGMII_01-TXCK/RMII1-TXCK/DMIC-DATA0/PG-EINT12
PG13/UART4-CTS/I2S1-DOU0/I2S1-DIN1/RGMII_01-TXCTL/RMII1-TXEN/DMIC-DATA1/PG-EINT13
PG14/CAN1-TX0/I2S1-DIN0/I2S1-DOU1/RGMII_01-MDC/DMIC-DATA2/LBUS-DRQ0/PG-EINT14
PG15/CAN1-RX0/RGMII_01-MDIO/DMIC-DATA3/LBUS-DRQ1/PG-EINT15

VCC-PG 1H1 3.3V VCC_PG

N3	RGMII_1	RXD3	RGMII_01	已经分配
P2	RGMII_1	RXD2	RGMII_01	已经分配
N2	RGMII_1	RXCK	RGMII_01	已经分配
N1	RGMII_1	TXD3	RGMII_01	已经分配
R1	RGMII_1	TXD2	RGMII_01	已经分配
P3	RGMII_1	EPHY CK25M	RGMII_01	已经分配
M3	RGMII_1	RXD1	RGMII_01	已经分配
M2	RGMII_1	RXD0	RGMII_01	已经分配
L3	RGMII_1	RXCTL	RGMII_01	已经分配
IJ2	RGMII_1	CLKIN	RGMII_01	已经分配
IH2	RGMII_1	TXD1	RGMII_01	已经分配
IJ3	RGMII_1	TXD0	RGMII_01	已经分配
IG3	RGMII_1	TXCK	RGMII_01	已经分配
IH3	RGMII_1	TXCTL	RGMII_01	已经分配
IG2	RGMII_1	MDC	RGMII_01	已经分配
IG1	RGMII_1	MDIO	RGMII_01	已经分配



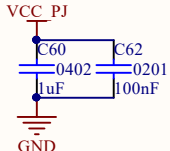
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U1M

GPIO-J

PJ0/PWM2-0/I2S2-MCLK/TW14-SCK/RGMII_00-RXD1/RMII0-RXD1/DMIC-CLK/UART5-TX/PJ-EINT0
PJ1/PWM2-1/I2S2-BCLK/TW14-SDA/RGMII_00-RXD0/RMII0-RXD0/DMIC-DATA0/UART5-RX/PJ-EINT1
PJ2/PWM2-2/I2S2-LRCK/RGMII_00-RXCTL/RMII0-CRS-DV/DMIC-DATA1/UART5-RTS/PJ-EINT2
PJ3/PWM2-3/I2S2-DOU0/I2S2-DIN1/RGMII_00-CLKIN/RMII0-RXER/DMIC-DATA2/UART5-CTS/PJ-EINT3
PJ4/PWM2-4/I2S2-DIN0/I2S2-DOU1/RGMII_00-TXD1/RMII0-TXD1/DMIC-DATA3/UART7-TX/PJ-EINT4
PJ5/PWM2-5/I2S2-DIN2/I2S2-DOU2/RGMII_00-TXD0/RMII0-TXD0/UART7-RX/PJ-EINT5
PJ6/PWM2-6/I2S2-DIN3/I2S2-DOU3/RGMII_00-TXCK/RMII0-TXCK/UART7-RTS/PJ-EINT6
PJ7/PWM2-7/I2S1-MCLK/RGMII_00-TXCTL/RMII0-TXEN/UART7-CTS/PJ-EINT7
PJ8/PWM2-8/I2S1-BCLK/LEDC/RGMII_00-MDC/UART2-TX/PJ-EINT8
PJ9/PWM2-9/I2S1-LRCK/IR-TX/RGMII_00-MDIO/UART2-RX/PJ-EINT9
PJ10/PWM2-10/I2S1-DOU0/I2S1-DIN1/RGMII_00-EPHY-25/50M/UART2-RTS/PJ-EINT10
PJ11/PWM2-11/I2S1-DIN0/I2S1-DOU1/RGMII_00-RXD3/RMII0-NULL/UART2-CTS/PJ-EINT11
PJ12/PWM2-12/CAN0-TX0/IR0-RX/RGMII_00-RXD2/RMII0-NULL/TW10-SCK/UART9-TX/PJ-EINT12
PJ13/PWM2-13/CAN0-RX0/IR1-RX/RGMII_00-RXCK/RMII0-NULL/TW10-SDA/UART9-RX/PJ-EINT13
PJ14/CAN1-TX0/IR2-RX/RGMII_00-TXD3/RMII0-NULL/TW15-SCK/UART9-RTS/PJ-EINT14
PJ15/CAN1-RX0/IR3-RX/RGMII_00-TXD2/RMII0-NULL/TW15-SDA/UART9-CTS/PJ-EINT15

VCC-PJ 1A3 3.3V VCC_PJ

C2	RGMII_0	RXD1	RGMII_00	已经分配
C1	RGMII_0	RXD0	RGMII_00	已经分配
C3	RGMII_0	RXCTL	RGMII_00	已经分配
B1	RGMII_0	CLKIN	RGMII_00	已经分配
A3	RGMII_0	TXD1	RGMII_00	已经分配
B3	RGMII_0	TXD0	RGMII_00	已经分配
A2	RGMII_0	TXCK	RGMII_00	已经分配
B2	RGMII_0	TXCTL	RGMII_00	已经分配
B4	RGMII_0	MDC	RGMII_00	已经分配
C4	RGMII_0	MDIO	RGMII_00	已经分配
E1	RGMII_0	EPHY CK25M	RGMII_00	已经分配
E3	RGMII_0	RXD3	RGMII_00	已经分配
D2	RGMII_0	RXD2	RGMII_00	已经分配
D3	RGMII_0	RXCK	RGMII_00	已经分配
IA2	RGMII_0	TXD3	RGMII_00	已经分配
IA1	RGMII_0	TXD2	RGMII_00	已经分配



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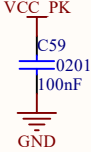
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GPIO-K

PK0/MCSIA-CKP/UART7-TX/TW11-SCK/NCIS-D0/LBUS-READY1/I2S1-MCLK/LBUS-DRQ0/LCD-D0/PK-EINT0
PK1/MCSIA-CKN/UART7-RX/TW11-SDA/NCIS-D1/LBUS-READY0/I2S1-BCLK/LBUS-DRQ1/LCD-D1/PK-EINT1
PK2/MCSIA-D0P/UART7-RTS/SPI1-CS0/NCIS-D2/LBUS-LD19/I2S1-LRCK/LBUS-LBE3/LCD-D8/PK-EINT2
PK3/MCSIA-D0N/UART7-CTS/SPI1-CLK/NCIS-D3/LBUS-LD18/I2S1-DOU0/LBUS-DE/LCD-D9/PK-EINT3
PK4/MCSIA-D1P/TW11-SCK/SPI1-MOSI/NCIS-D4/LBUS-LD17/I2S1-DIN0/LBUS-CS3/LCD-D16/PK-EINT4
PK5/MCSIA-D1N/TW11-SDA/SPI1-MISO/NCIS-D5/LBUS-LD16/LBUS-CS2/LCD-D17/PK-EINT5
PK6/MCSIB-D1P/MCSIA-D2P/MCSIO-MCLK/SPI1-WP/NCIS-D6/LBUS-LD15/UART3-RTS/RGMII_00-RXD3/RMII0-NULL/PK-EINT6
PK7/MCSIB-D1N/MCSIA-D2N/TW12-SCK/SPI1-HOLD/NCIS-D7/LBUS-LD14/RGMII_00-RXD2/RMII0-NULL/PK-EINT7
PK8/MCSIB-D0P/MCSIA-D3P/TW12-SDA/SPI1-CS1/NCIS-PCLK/LBUS-LD13/CLK-FANOUT0/RGMII_00-RXCK/RMII0-NULL/PK-EINT8
PK9/MCSIB-D0N/MCSIA-D3N/MCSII-MCLK/SPI1-CS2/NCIS-MCLK/LBUS-LD12/UART3-TX/CLK-FANOUT1/RGMII_00-TXD3/RMII0-NULL/PK-EINT9
PK10/MCSIB-CKP/TW13-SCK/PWM0-6/NCIS-HSYNC/LBUS-WAIT/UART3-RX/LBUS-LBE2/RGMII_00-TXD2/RMII0-NULL/PK-EINT10
PK11/MCSIB-CKN/TW13-SDA/PWM0-7/NCIS-VSYNC/LBUS-CS2/UART3-CTS/PK-EINT11

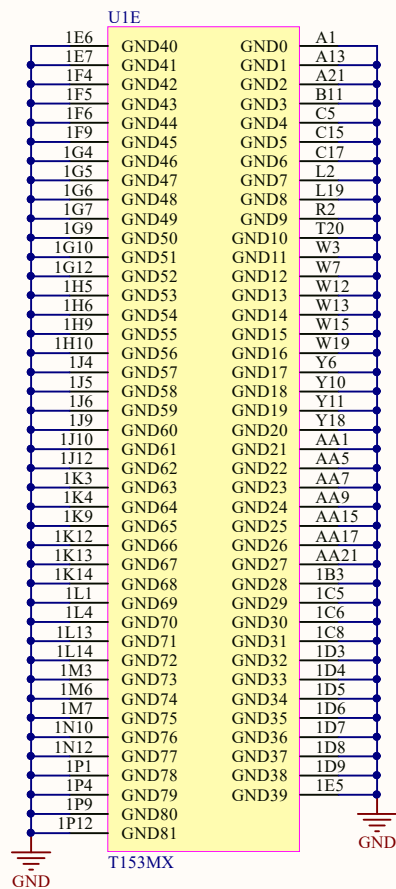
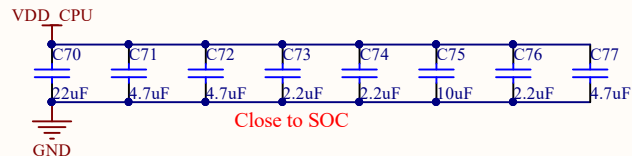
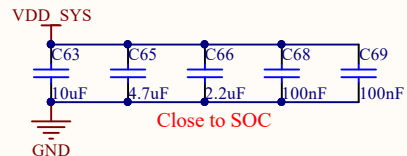
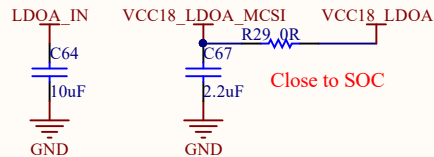
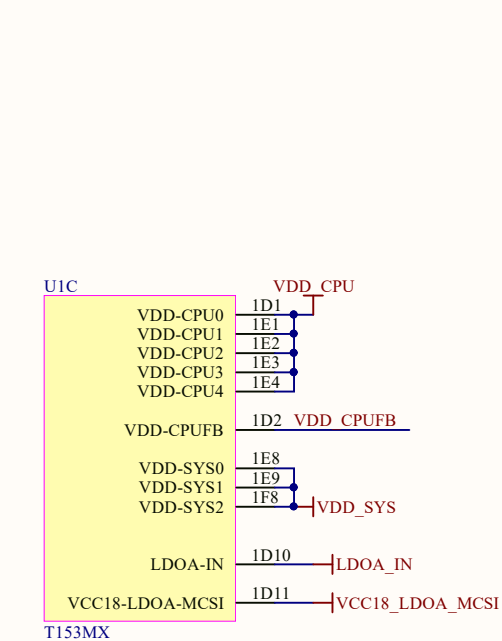
B19	MCSIA	CKP	MCSIA	已经分配
A19	MCSIA	CKN	MCSIA	已经分配
C18	MCSIA	D0P	MCSIA	已经分配
B18	MCSIA	D0N	MCSIA	已经分配
B20	MCSIA	D1P	MCSIA	已经分配
A20	MCSIA	D1N	MCSIA	已经分配
B17	MCSIB	D1P	MCSIB	已经分配
A17	MCSIB	D1N	MCSIB	已经分配
B15	MCSIB	D0P	MCSIB	已经分配
A15	MCSIB	D0N	MCSIB	已经分配
C16	MCSIB	CKP	MCSIB	已经分配
B16	MCSIB	CKN	MCSIB	已经分配

1B9 1.8V VCC_PK



T153MX

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