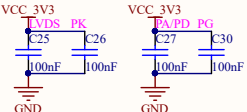


▲ Note: Keep away from the board outline and sensitive signal .Around GND line on PCB layout

Title			
Size A4	Number		Revision
Date:	2026/7/31	Sheet	of
File:	100ask Team inc		

	1	2	3	4	5	6	7	8														
A																						
B	UIB																					
	PA0/GPADC2-0/TP-X1/LBUS-READY3/LBUS-LBE2/PA-EINT0 PA1/GPADC2-1/TP-X2/LBUS-READY2/LBUS-DP1/PA-EINT1 PA2/GPADC2-2/TP-Y1/LBUS-DP2/PA-EINT2 PA3/GPADC2-3/TP-Y2/LBUS-DP3/PA-EINT3 PA4/GPADC0-0/RGMII0-RXD1/RMII0-RXD1/NCST-HSYNC/LBUS-CLK/PA-EINT4 PA5/GPADC0-1/RGMII0-RXD0/RMII0-RXD0/NCST-VSYNC/LBUS-ALF/PA-EINT5 PA6/GPADC0-2/RGMII0-RXCTL/RMII0-CRS-DV/NCST-MCLK/LBUS-CS1/PWMCS-18/LCD-D0/PA-EINT6 PA7/GPADC0-3/RGMII0-CLKIN/RMII0-RXER/NCST-PCLK/LBUS-CS0/PWMCS-19/LCD-D1/PA-EINT7 PA8/GPADC0-4/RGMII0-TXD1/RMII0-TXD1/NCST-D0/LBUS-DP0/PWMCS-1A/LCD-D8/PA-EINT8 PA9/GPADC0-5/RGMII0-TXD0/RMII0-TXD0/NCST-D1/LBUS-DP1/PWMCS-1B/LBUS-LBE3/LCD-D9/PA-EINT9 PA10/GPADC0-6/RGMII0-TXCK/RMII0-TXCK/NCST-D2/LBUS-LBE0/PWMCS-1C/LCD-D16/PA-EINT10 PA11/GPADC0-7/RGMII0-TXCTL/RMII0-TXEN/NCST-D3/LBUS-LBE1/PWMCS-1D/LCD-D17/PA-EINT11 PA12/GPADC0-8/RGMII0-MDC/NCST-D4/LBUS-LD8/PWMCS-1E/PA-EINT12 PA13/GPADC0-9/RGMII0-MDIO/NCST-D5/LBUS-LD9/PWMCS-1F/PA-EINT13 PA14/GPADC1-0/RGMII0-EPHY-25/50M/NCST-D6/LBUS-LD10/UART6-TX/PA-EINT14 PA15/GPADC1-1/RGMII0-RXD3/RMII0-NUL/NCST-D7/LBUS-LD11/UART6-RX/PA-EINT15				87 86 88 89 79 78 77 76 83 82 80 81 85 84 75 74	TP_X1 TP_X2 TP_Y1 TP_Y2 GPADC0 PA5 PA6 PA7 PA8 PA9 PA10 PA11 PA12 PA13 PA14 PA15	已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配															
C	PD0/LCD-D2/LVDS0-D0P/DSI-D0P/PWM0-0/UART2-CTS/UART9-TX/LBUS-LD20/PWMCS-00/RGMII2-RXD1/RMII2-RXD1/PD-EINT0 PD1/LCD-D3/LVDS0-D0N/DSI-D0N/PWM0-1/UART2-RTS/UART9-RX/LBUS-LD21/PWMCS-01/RGMII2-RXD0/RMII2-RXD0/PD-EINT1 PD2/LCD-D4/LVDS0-D1P/DSI-D1P/PWM0-2/UART2-TX/UART9-RTS/LBUS-LD22/PWMCS-02/RGMII2-RXCTL/RMII2-CRS-DV/PD-EINT2 PD3/LCD-D5/LVDS0-D1N/DSI-D1N/PWM0-3/UART2-RX/UART9-CTS/LBUS-LD23/PWMCS-03/RGMII2-CLKIN/RMII2-RXER/PD-EINT3 PD4/LCD-D6/LVDS0-D2P/DSI-D2P/PWM0-4/UART4-CTS/UART6-TX/LBUS-LD24/PWMCS-04/RGMII2-TXD1/RMII2-TXD1/PD-EINT4 PD5/LCD-D7/LVDS0-D2N/DSI-D2N/PWM0-5/UART4-RTS/UART6-RX/LBUS-LD25/PWMCS-05/LBUS-CS3/RGMII2-TXD0/RMII2-TXD0/PD-EINT5 PD6/LCD-D10/LVDS0-CKP/DSI-D2P/PWM0-6/UART4-TX/UART6-RTS/LBUS-LD26/PWMCS-06/LBUS-CS2/RGMII2-TXCK/RMII2-TXCK/PD-EINT6 PD7/LCD-D11/LVDS0-CKN/DSI-D2N/PWM0-7/UART4-RX/UART6-CTS/LBUS-LD27/PWMCS-07/LBUS-READY3/RGMII2-TXCTL/RMII2-TXEN/PD-EINT7 PD8/LCD-D12/LVDS0-D3P/DSI-D3P/PWM1-0/CAN1-TX0/LBUS-LD28/PWMCS-08/LBUS-DE/RGMII2-MDC/PD-EINT8 PD9/LCD-D13/LVDS0-D3N/DSI-D3N/PWM1-1/DMIC-CLK/CAN1-RX0/LBUS-LD29/PWMCS-09/LBUS-READY2/RGMII2-MDIO/PD-EINT9 PD10/LCD-D14/LVDS1-D0P/SP11-CS0/DBI-CSX/PWM1-2/DMIC-DATA0/UART8-TX/LBUS-LD30/PWMCS-0A/LBUS-READY1/PD-EINT10 PD11/LCD-D15/LVDS1-D0N/SP11-CLK/DBI-SCLK/PWM1-3/DMIC-DATA1/UART8-RX/LBUS-LD31/PWMCS-0B/LBUS-READY0/PD-EINT11 PD12/LCD-D18/LVDS1-D1P/SP11-MOSI/DBI-SDO/PWM1-4/DMIC-DATA2/UART8-RTS/LBUS-LD0/PWMCS-0C/PD-EINT12 PD13/LCD-D19/LVDS1-D1N/SP11-MISO/DBI-SDI/DBI-TE/DBI-DCX/PWM1-5/DMIC-DATA3/UART8-CTS/LBUS-LD1/PWMCS-0D/PD-EINT13 PD14/LCD-D20/LVDS1-D2P/SP11-HOLD/DBI-DCX/DBI-WRX/PWM1-6/UART3-TX/UART8-DCD/LBUS-LD2/PWMCS-0E/PD-EINT14 PD15/LCD-D21/LVDS1-D2N/SP11-WP/DBI-TE/PWM1-7/UART3-RX/UART8-DSR/LBUS-LD3/PWMCS-0F/PD-EINT15 PD16/LCD-D22/LVDS1-CKP/PWM2-0/UART3-RTS/UART8-DTR/LBUS-LD4/PWMCS-10/RGMII2-EPHY-25/50M/PD-EINT16 PD17/LCD-D23/LVDS1-CKN/PWM2-1/UART3-CTS/UART8-R/LBUS-LD5/PWMCS-11/RGMII2-RXD3/RMII2-NUL/PD-EINT17 PD18/LCD-CLK/LVDS1-D3P/TW15-SCK/PWM2-2/CAN0-TX0/LBUS-LD6/PWMCS-12/RGMII2-RXD2/RMII2-NUL/PD-EINT18 PD19/LCD-DE/LVDS1-D3N/TW15-SDA/PWM2-3/CAN0-RX0/LBUS-LD7/PWMCS-13/RGMII2-RXCK/RMII2-NUL/PD-EINT19 PD20/LCD-HSYNC/PWM2-4/UART3-TX/TW15-SCK/LBUS-WR/PWMCS-14/RGMII2-TXD3/RMII2-NUL/PD-EINT20 PD21/LCD-VSYNC/PWM2-5/UART3-RX/TW15-SDA/LBUS-OE/PWMCS-15/RGMII2-TXD2/RMII2-NUL/PD-EINT21				61 60 63 62 65 64 68 67 70 69 50 49 52 51 55 54 57 56 59 58 72 71	PD0 PD1 PD2 PD3 PD4 PD5 PD6 PD7 PD8 PD9 PD10 PD11 PD12 PD13 PD14 PD15 PD16 PD17 PD18 PD19 PD20 PD21	已经分配 已经分配															
	VCC-PA-PD VCC18-LVDS				66 53	VCC_3V3 VCC_1V8																
D	GPIO-G				21 20 15 27 26 13 18 17 16 14 25 24 22 23 29 28	R27 33R PG0 PG1 PG2 PG3 PG4 PG5 PG6 PG7 PG8 PG9 PG10 PG11 PG12 PG13 PG14 PG15	已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配 已经分配															
	GPIO-K				92 93 95 94 97 96 98 99 100 101 102	PK0 PK1 PK2 PK3 PK4 PK5 PK6 PK7 PK8 PK9 PK10																
	T153M3				91	VCC_3V3																
<table><tr><td colspan="3">Title</td></tr><tr><td>Size</td><td>Number</td><td>Revision</td></tr><tr><td>A3</td><td></td><td></td></tr><tr><td>Date:</td><td>2026/7/31</td><td>Sheet of</td></tr><tr><td>File:</td><td>100askTeam inc</td><td></td></tr></table>								Title			Size	Number	Revision	A3			Date:	2026/7/31	Sheet of	File:	100askTeam inc	
Title																						
Size	Number	Revision																				
A3																						
Date:	2026/7/31	Sheet of																				
File:	100askTeam inc																					
	1	2	3	4	5	6	7	8														



U1C

GPIO-C

PC0/SDC2-CLK/SPI0-CLK/SPIF0-CLK/PC-EINT0
PC1/SDC2-CMD/SPI0-CS0/SPIF0-CS0/PC-EINT1
PC2/SDC2-D2/SPI0-MISO/SPIF0-MISO/BOOT-SEL0/PC-EINT2
PC3/SDC2-D1/SPI0-MISO/SPIF0-MISO/BOOT-SEL1/PC-EINT3
PC4/SDC2-D0/SPI0-WP/SPIF0-WP/CAN1-TX0/UART6-TX/PC-EINT4
PC5/SDC2-D3/SPI0-HOLD/SPIF0-HOLD/CAN1-RX0/UART6-RX/PC-EINT5
PC6/SDC2-D4/SPI0-CS1/SPIF0-D4/CAN0-TX0/PC-EINT6
PC7/SDC2-D5/SPIF0-D5/CAN0-RX0/PC-EINT7
PC8/SDC2-D6/SPIF0-D6/UART5-CTS/PC-EINT8
PC9/SDC2-D7/SPIF0-D7/UART5-TX/PC-EINT9
PC10/SDC2-DS/SPIF0-DQS/UART5-RX/PC-EINT10
PC11/SDC2-RST/UART5-RTS/PC-EINT11

VCC-PC

GPIO-F

PF0/SDC0-D1/JTAG-MS/UART1-TX/RJTAG-MS/IR0-RX/LBUS-DRQ0/PF-EINT0
PF1/SDC0-D0/JTAG-DI/UART1-RX/RJTAG-DI/IR1-RX/LBUS-DRQ1/PF-EINT1
PF2/SDC0-CLK/UART0-TX/DMIC-CLK/SPI3-CLK/IR2-RX/PF-EINT2
PF3/SDC0-CMD/JTAG-DO/DMIC-DATA0/SPI3-CS0/RJTAG-DO/IR3-RX/PF-EINT3
PF4/SDC0-D3/UART0-RX/DMIC-DATA1/SPI3-MOSI/IR-TX/PF-EINT4
PF5/SDC0-D2/JTAG-CK/DMIC-DATA2/SPI3-MISO/RJTAG-CK/PF-EINT5

GPIO-B

PB8/OWA-OUT/I2S0-DIN0/I2S0-DOUT1/PWM0-0/CAN1-RX0/UART8-DCD/PB-EINT8
PB9/UART0-TX/I2S0-DIN2/I2S0-DOUT2/TWI0-SCK/UART8-DSR/PB-EINT9
PB10/UART0-RX/I2S0-DIN3/I2S0-DOUT3/PWM0-1/TWI0-SDA/UART8-DTR/PB-EINT10
PB11/TWI5-SCK/UART7-RTS/SPI3-CS0/PWM0-2/WATCHDOG-SIG/UART8-RI/RGMII2-MDC/PB-EINT11
PB12/TWI5-SDA/UART7-CTS/SPI3-CLK/PWM0-3/PLL-LOCK-DBG/RGMII2-MDIO/PB-EINT12
PB13/TWI4-SCK/UART7-TX/SPI3-MOSI/PWM0-4/IR2-RX/CLK-FANOUT0/RGMII2-EPHY-25/50M/PB-EINT13
PB14/TWI4-SDA/UART7-RX/SPI3-MISO/PWM0-5/IR3-RX/CLK-FANOUT1/PB-EINT14

VCC-IO

31 R13 33R SPI0_CLK
30 SPI0_CS
41 BOOT_SEL0 FEL SPI0_MOSI
39 BOOT_SEL1 SPI0_MISO
37 SPI0_WP
35 SPI0_HOLD
36 PC6
38 PC7
40 PC8
42 PC9
34 PC10
33 PC11

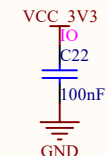
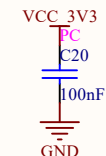
VCC_3V3

121 SDC0_D1 SDC0 已经分配
122 SDC0_D0 SDC0 已经分配
123 R16 33R SDC0_CLK SDC0 已经分配
124 SDC0_CMD SDC0 已经分配
125 SDC0_D3 SDC0 已经分配
126 SDC0_D2 SDC0 已经分配

9 PB8 已经分配
8 UART0_TX 已经分配
7 UART0_RX 已经分配
6 PB11 已经分配
5 PB12 已经分配
4 PB13 已经分配
3 PB14 已经分配

VCC_3V3

SPI NAND
SPI NAND
SPI NAND/BOOT_CFG/FEL
SPI NAND/BOOT_CFG
SPI NAND
SPI NAND
已经分配
已经分配
已经分配
已经分配

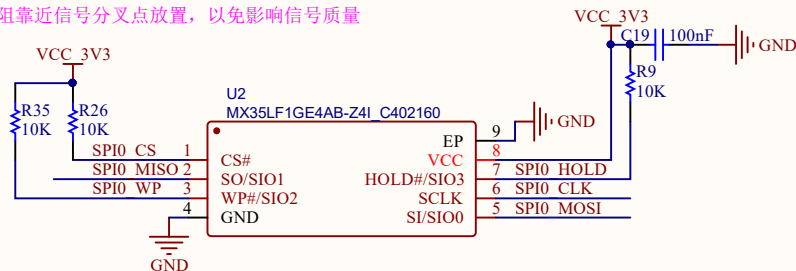


T153M3

SPI NAND

BOOT_SEL0 R28 4.7K NC
BOOT_SEL1 R2 4.7K NC

电阻靠近信号分叉点放置，以免影响信号质量



BOOT_SEL1 BOOT_SEL0
0 0 SPI0-NAND->SPI0 NOR(4线)->SPI0 NOR(1线)->USB
0 1 SPI0 NOR(4线)->SPI0 NOR(1线)->SPI0-NAND->USB
1 0 SDC0->SPI0-NAND->SPI0 NOR(4线)->SPI0 NOR(1线)->USB
1 1 SDC0->SPI2 NOR(4线)->SPI2 NOR(1线)->SPI2-NAND->EMMC2_USR->EMMC2_BOOT->SPI0_NAND->USB

说明：BOOT_SEL在芯片内部默认上拉，PIN浮空时为高电平

Title		
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A4		
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